

What is claimed is:

1. A method for generating random number, comprising the steps of:
preparing a bistable multivibrator circuit comprised of a first transistor and
a second transistor,
applying a driving voltage to said bistable multivibrator circuit to switch on
and off one of said first transistor and said second transistor randomly,
allotting numerals "0" and "1" to on-state and off-state of said one of said
first transistor and said second transistor, thereby to generate a binary random
number.
2. The generating method as defined in claim 1, wherein said on-state
and said off-state of said one of said first transistor and said second transistor is
detected by measuring collector voltage thereof.
3. The generating method as defined in claim 1, wherein occurrence
probability of "0" and "1" is controlled by adjusting characteristic value of a
circuit component in said bistable multivibrator circuit.
4. The generating method as defined in claim 3, wherein said occurrence
value is set to 0.5.
5. The generating method as defined in claim 3, wherein said circuit
component is a biasing variable resistance.
6. A random number generator comprising a bistable multivibrator
circuit.
7. The random number generator as defined in claim 6, wherein said
bistable multivibrator circuit includes a biasing variable resistance.
8. The random number generator as defined in claim 6, further
comprising an electric power supply controlling circuit which is coupled to said
bistable multivibrator circuit and generates a driving voltage for said bistable
multivibrator circuit.
9. The random number generator as defined in claim 6, further
comprising a buffer circuit which is coupled to said one of said first transistor
and said second transistor and detect collector voltage thereof.